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APPROVED	DATE
BY	DATE
DRAFTSMAN	

Fig. 2A

INPUTTED ORIGINAL BIT STREAM A

	A1	A2	A3	...
--	----	----	----	-----

Fig. 2B

CONVERTED BIT STREAM B
OUTPUTTED BY TRANSCODER 1

	B1	B2	B3	...
--	----	----	----	-----

Fig. 2C

ORIGINAL BIT STREAM A
OUTPUTTED BY BUFFER 2

	A1	A2	A3	...
--	----	----	----	-----

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Fig. 4A

INPUTTED ORIGINAL BIT STREAM A

	A1	A2	A3	...
--	----	----	----	-----

Fig. 4B

CONVERTED BIT STREAM B
OUTPUTTED BY TRANSCODER 11

tp

	B1	B2	B3	...
--	----	----	----	-----

Fig. 4C

BUFFER CONTROLLER 14



Fig. 4D

ORIGINAL BIT STREAM A
OUTPUTTED BY BUFFER 12

	A1	A2	A3	...
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Fig. 5A
ORIGINAL OR CONVERTED BIT STREAM

Fig. 5B
SWITCH COMMAND

Fig. 5C
SWITCH CONTROLLER 15

